

Block diagram

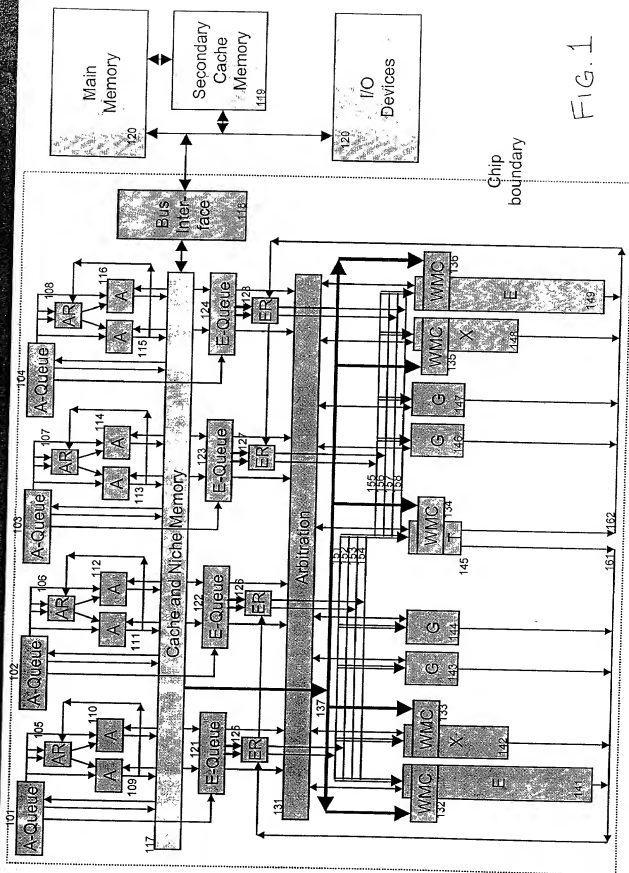


FIG. 1

Wide multiply matrix

$$\blacksquare \text{rd}_{128} = \text{m}[\text{rc}]_{(128 \times 64 / \text{size})} * \text{rb}_{128}$$

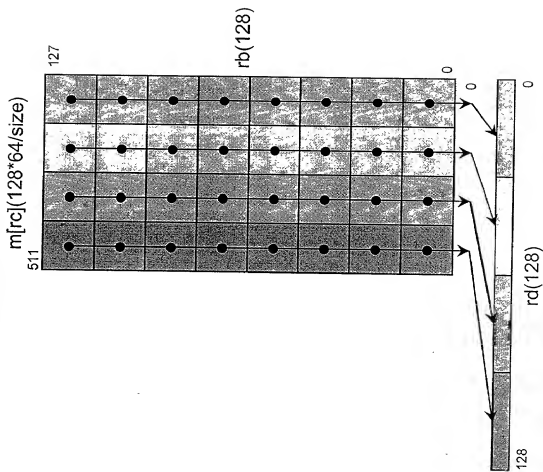


FIG. 2

Wide multiply matrix

511

mrc(128*64)size

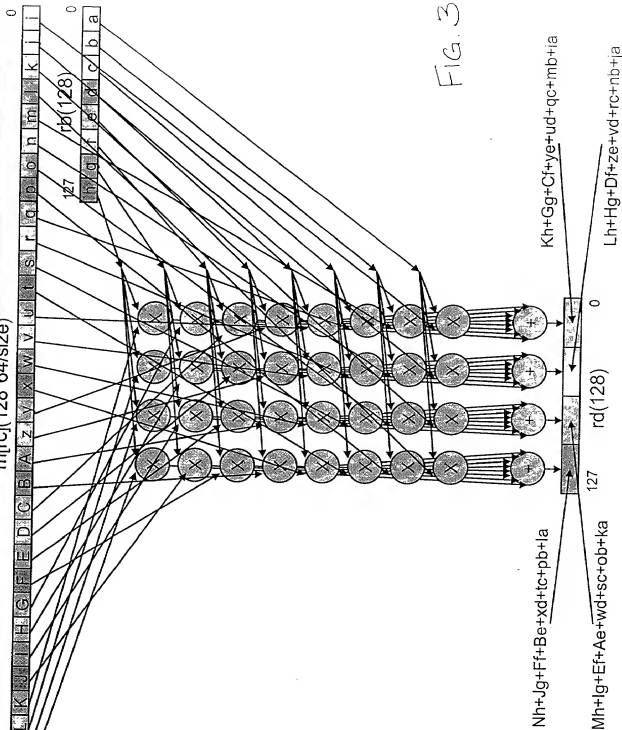
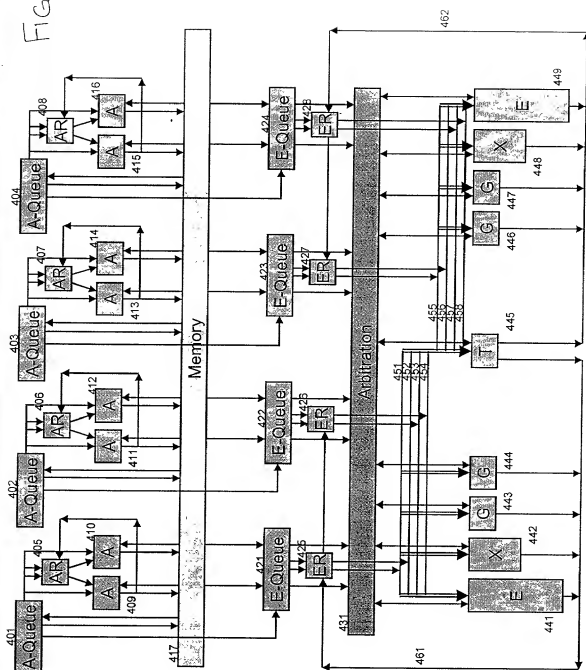


FIG. 3

FIG. 4



Wide operand specifier

h g u

```

    specifier=address+(size/2)+(width/2)

```

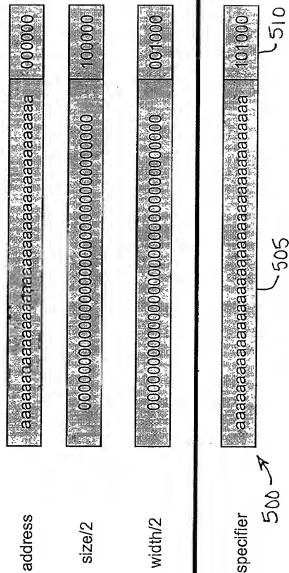
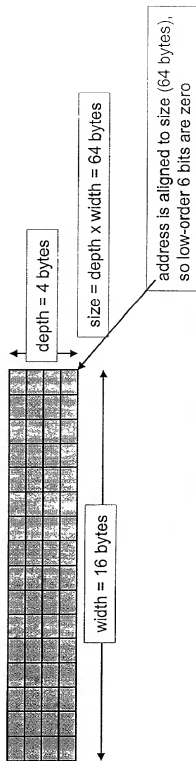
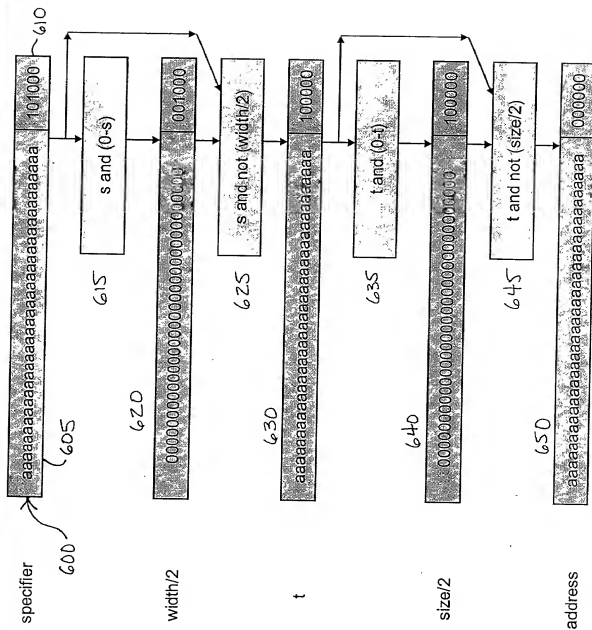
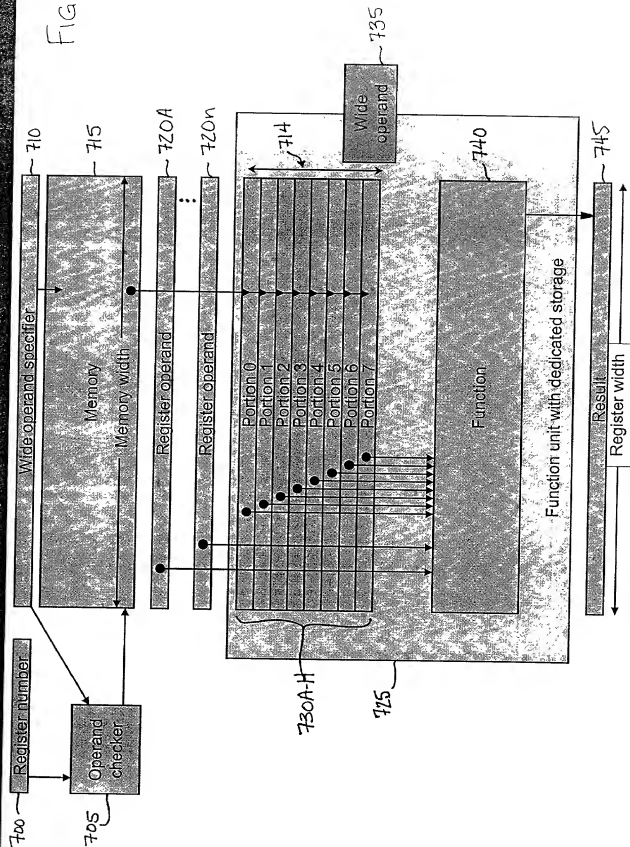


Fig. 6

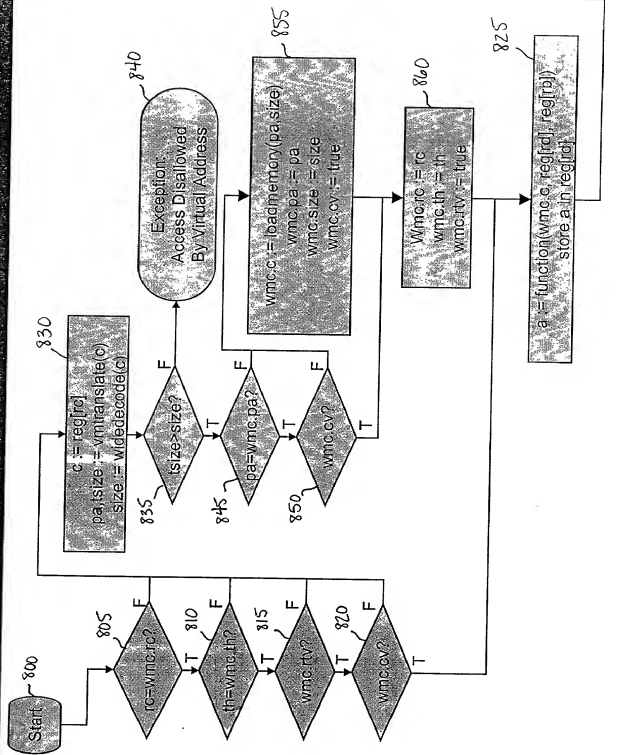


Wide function unit



Wide MicroCache control

Fig. 8



Wide MicroCache data structures

■ wmc.c contents

Fig. 9



■ wmc.pa - physical address



■ wmc.size - size of contents



■ wmc.cv - contents valid



■ wmc.th - thread last used



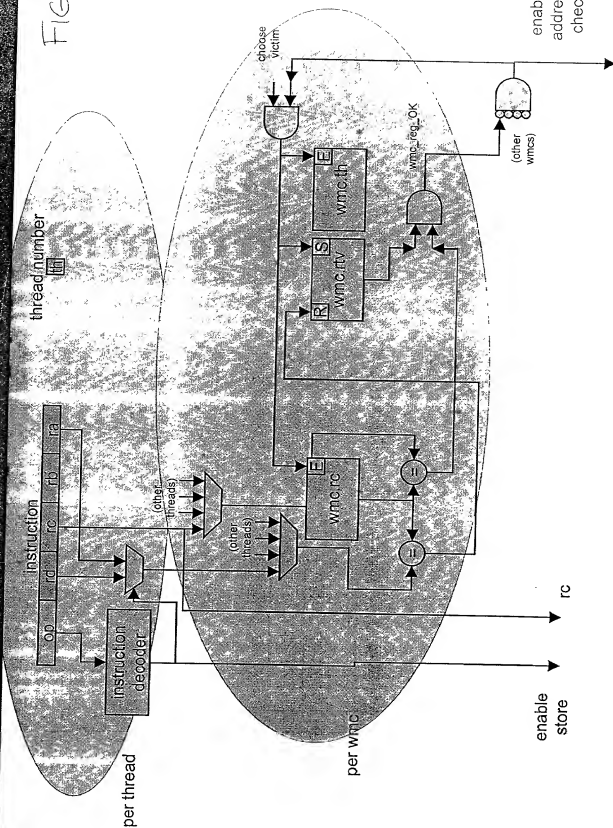
■ wmc.reg - register last used



■ wmc.rtv - register & thread valid



FIG. 10



Wide MicroCache control (2)

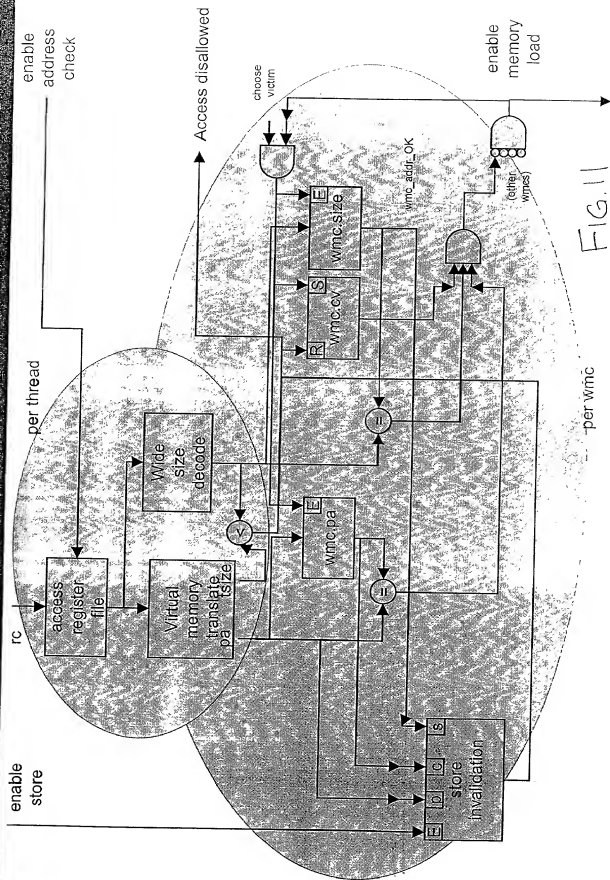


FIG. 11